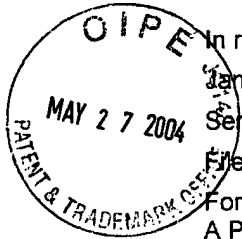


PATENT
02-ZAV-221

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re Patent Application of:

Jan Frans Lucien CRANINCKX

Serial No.: 10/718,256

Filed: November 20, 2003

For: LOW FREQUENCY SELF-CALIBRATION OF
A PLL WITH MULTIPHASE CLOCKS

: Group Art Unit: 2681

: Confirmation No.: 8145

INFORMATION DISCLOSURE STATEMENTMail Stop Missing Parts
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The attached Form PTO-1449 provides a listing of information which may be relevant to the subject application. This IDS is not intended as a representation that better art is not available, nor that other art than that identified exists; nor that the information provided is prior art; nor that a search has been made.

This IDS is submitted under:

☒ XX 37 CFR 1.97(b) - No Fee.☐ 37 CFR 1.97(c) - No Fee, with Certification.☐ 37 CFR 1.97(c) - Fee.☐ 37 CFR 1.97(d) - Fee and Certification.

The Commissioner is authorized to charge any required fees under 37 CFR 1.17(p) and (i)(1) to Deposit Account No. 50-1556.

Respectfully submitted,

Date: 5/21/04By: 

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service by first class mail in an envelope addressed to: Mail Stop Missing Parts, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Taragowski
Name of Person Mailing PaperKaren Taragowski
Signature of Person Mailing Paper5/24/04
Date of Deposit

Form PTO-1449		U.S. Dept. of Commerce Patent & Trademark Office		Atty. Docket: 02-ZAV-221		Serial No. 10/718,256	
List of Documents Cited by Applicant (Use several sheets if necessary)				Applicant: Jan Frans Lucien CRANINCKX			
				Filing Date: November 20, 2003			
				Group: 2681			
U.S. PATENT DOCUMENTS							
Document Number		Date		Name		Filing Date, if applicable	
AA1		6,064,272		May 16, 2000		Rhee	
AA2		6,181,213		January 30, 2001		Chang	
FOREIGN PATENT DOCUMENTS							
Document Number		Date		Country		Trans'l'n Yes/No	
AA3		EP 1 133 060		September 12, 2001		Europe	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
AA4		Park, Chan-Hong, "A 1.8-GHz Self-Calibrated Phase-Locked Loop with Precise I/Q Matching", IEEE Journal of Solid-State Circuits, Vol. 36, No. 5, May 2001, pp. 777-783, XP002241677.					
AA5		European Search Report dated May 20, 2003 for European Application No. 02447228.4.					
Examiner:				Date Considered:			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							